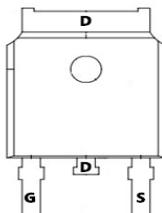
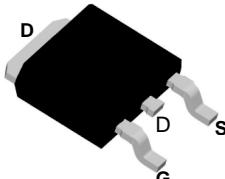
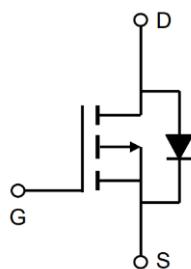


**TM15P06D**
**P -Channel Enhancement Mosfet**

<b>General Description</b> <ul style="list-style-type: none"> <li>• Low <math>R_{DS(ON)}</math></li> <li>• RoHS and Halogen-Free Compliant</li> </ul> <b>Applications</b> <ul style="list-style-type: none"> <li>• Load switch</li> <li>• PWM</li> </ul>	<b>General Features</b> <p> <math>V_{DS} = -60V</math> <math>I_D = -15A</math>  <math>R_{DS(ON)} = 70m\Omega</math>(typ.) @ <math>V_{GS} = -10V</math>          100% UIS Tested          100% <math>R_g</math> Tested       </p> 			
 D:TO-252-3L	 			
Marking: 15P06				
<b>Absolute Maximum Ratings</b> ( $T_A = 25^\circ C$ Unless Otherwise Noted)				
Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	-60	V	
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V	
$I_D @ T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-15	A	
$I_D @ T_A=70^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-9.7	A	
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-56	A	
EAS	Single Pulse Avalanche Energy <sup>3</sup>	29.8	mJ	
$I_{AS}$	Avalanche Current	24.4	A	
$P_D @ T_C=25^\circ C$	Total Power Dissipation <sup>4</sup>	31.3	W	
$P_D @ T_A=25^\circ C$	Total Power Dissipation <sup>4</sup>	2	W	
$T_{STG}$	Storage Temperature Range	-55 to 150	°C	
$T_J$	Operating Junction Temperature Range	-55 to 150	°C	
<b>Thermal Data</b>				
Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	4	°C/W

**P-Channel Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=-250\mu\text{A}$	-60	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^{\circ}\text{C}$ , $I_D=-1\text{mA}$	---	-0.023	---	$\text{V}/^{\circ}\text{C}$
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=-10\text{V}$ , $I_D=-10\text{A}$	---	70	82	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$ , $I_D=-5\text{A}$	---	83	98	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D = -250\mu\text{A}$	-1.0	1.6	-2.5	V
$\Delta V_{\text{GS}(\text{th})}$	$V_{\text{GS}(\text{th})}$ Temperature Coefficient		---	4.65	---	$\text{mV}/^{\circ}\text{C}$
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-48\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^{\circ}\text{C}$	---	---	1	$\text{uA}$
		$V_{\text{DS}}=-48\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^{\circ}\text{C}$	---	---	5	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$ , $I_D=-4\text{A}$	---	8.7	---	S
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	15	---	$\Omega$
$Q_g$	Total Gate Charge (-4.5V)	$V_{\text{DS}}=-12\text{V}$ , $V_{\text{GS}}=-4.5\text{V}$ , $I_D=-6\text{A}$	---	11.8	---	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge		---	1.9	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	6.5	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=-15\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $R_G=3.3\Omega$ , $I_D=-1\text{A}$	---	8.8	---	$\text{ns}$
$T_r$	Rise Time		---	19.6	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	47.2	---	
$T_f$	Fall Time		---	9.6	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	1080	---	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		---	73	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	50	---	

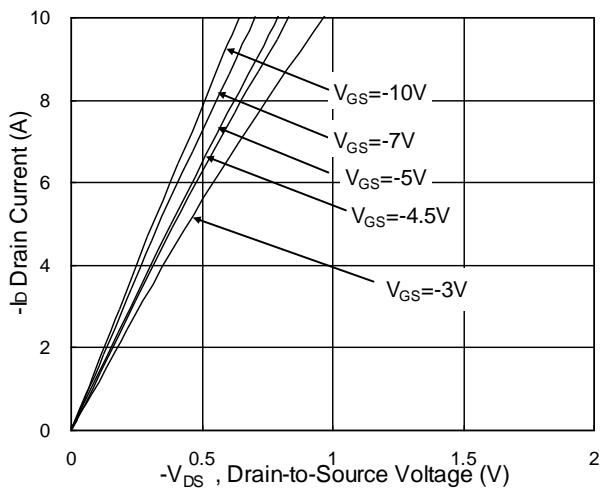
**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current <sup>1,5</sup>	$V_G=V_D=0\text{V}$ , Force Current	---	---	-15	A
$I_{\text{SM}}$	Pulsed Source Current <sup>2,5</sup>		---	---	-26	A
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=-1\text{A}$ , $T_J=25^{\circ}\text{C}$	---	---	-1	V

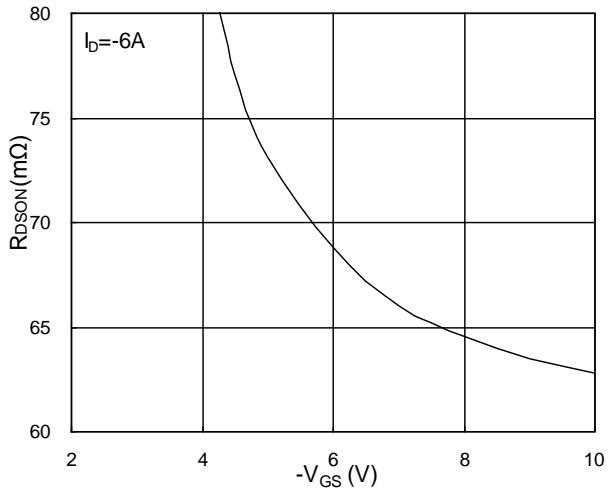
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=-25\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $L=0.1\text{mH}$ , $I_{\text{AS}}=-24.4\text{A}$
- 4.The power dissipation is limited by  $150^{\circ}\text{C}$  junction temperature
- 5.The data is theoretically the same as  $I_D$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.

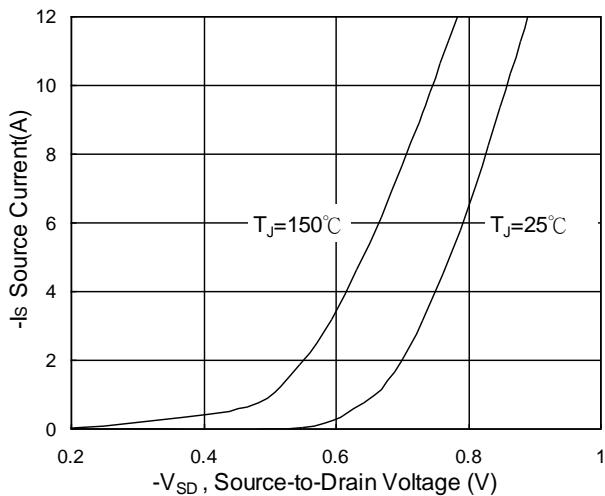
### P-Channel Typical Characteristics



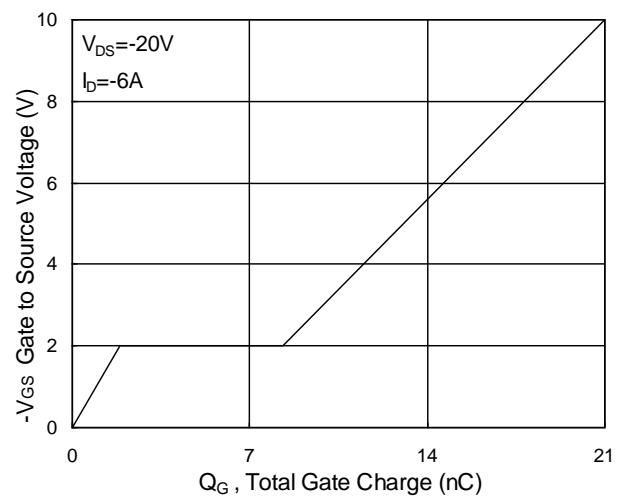
**Fig.1 Typical Output Characteristics**



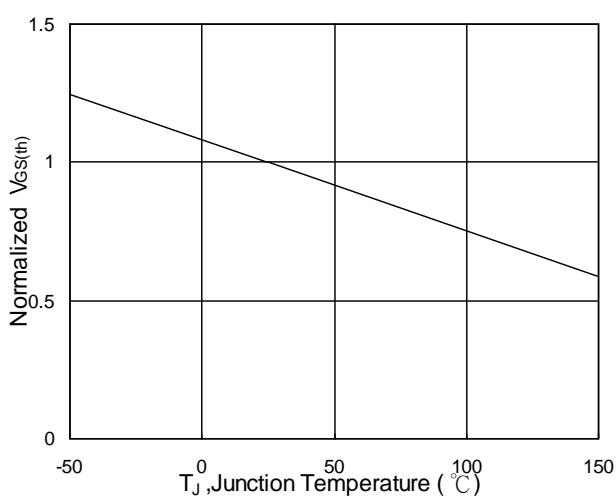
**Fig.2 On-Resistance v.s Gate-Source**



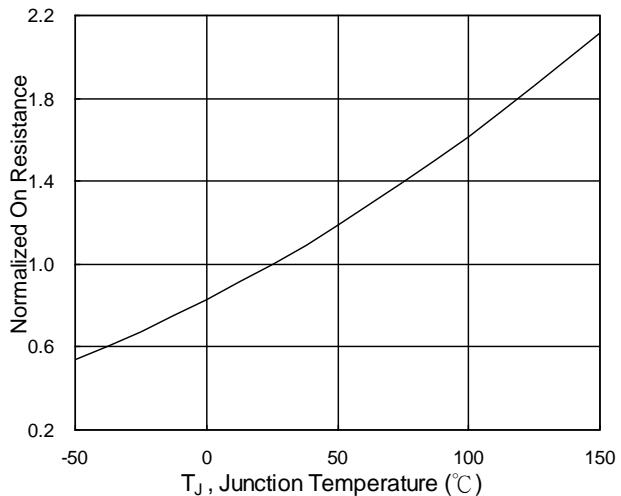
**Fig.3 Forward Characteristics of Reverse**



**Fig.4 Gate-Charge Characteristics**



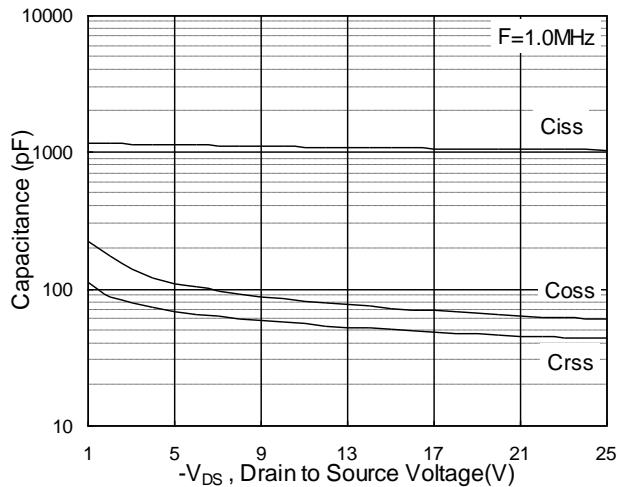
**Fig.5 Normalized  $V_{GS(th)}$  v.s  $T_J$**



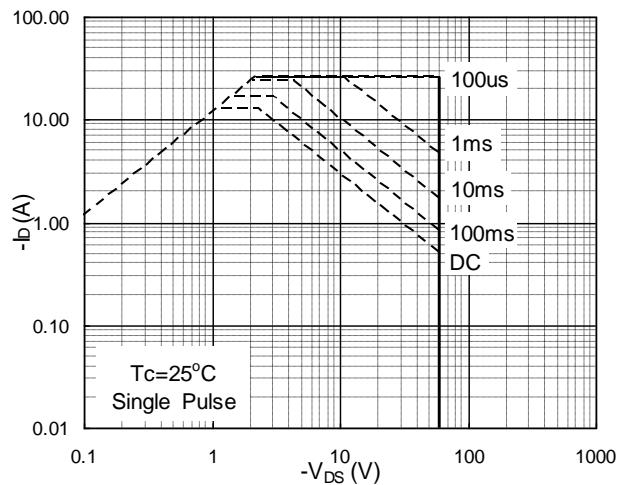
**Fig.6 Normalized  $R_{DS(on)}$  v.s  $T_J$**

## TM15P06D

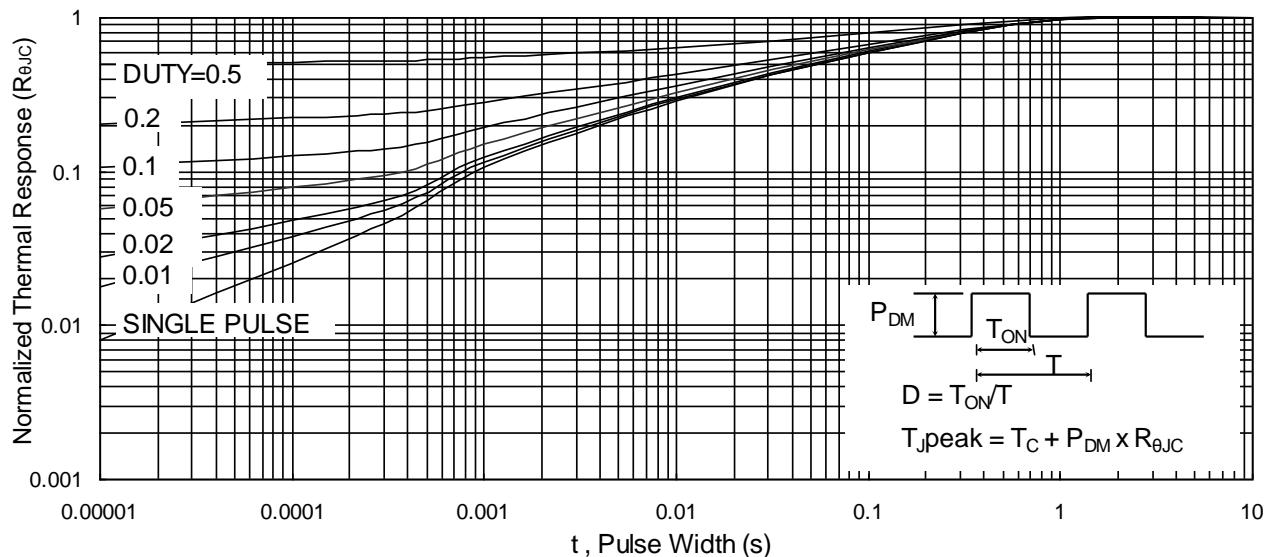
## P -Channel Enhancement Mosfet



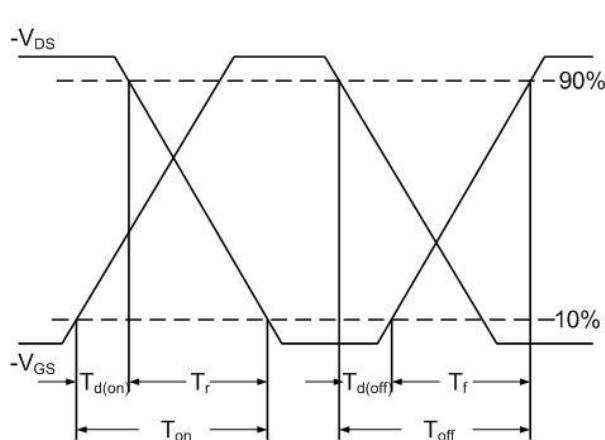
**Fig.7 Capacitance**



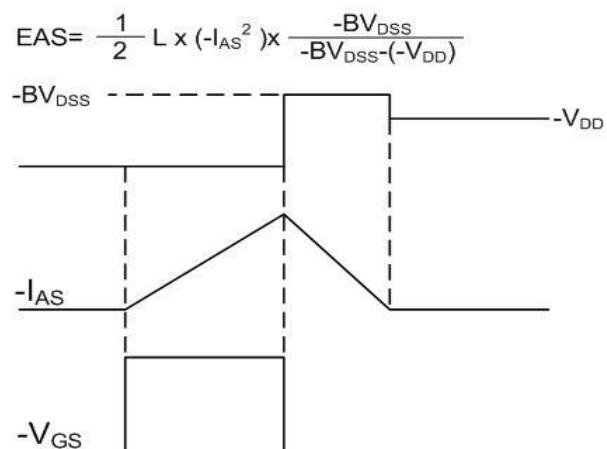
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

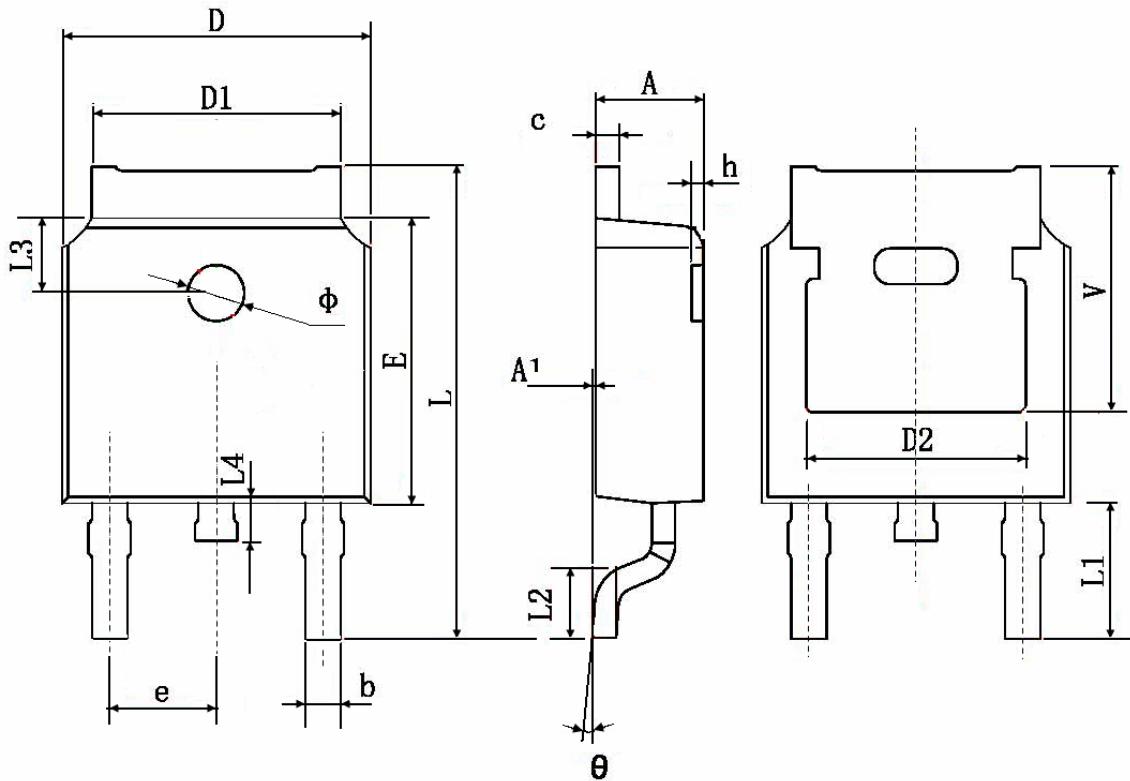


**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**

## Package Information:TO-252-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	